

WEST Search History



DATE: Monday, September 27, 2004

Hide?	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
	<i>DB=USPT; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L25	L24 and l21	8
<input type="checkbox"/>	L24	thread same simulat\$4	897
<input type="checkbox"/>	L23	L22 and static\$6 adj3 thread	1
<input type="checkbox"/>	L22	l21 and thread schedul\$4	45
<input type="checkbox"/>	L21	718/102.ccls.	668
<input type="checkbox"/>	L20	static\$10 adj3 thread same linked list	0
	<i>DB=USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L19	(stor\$6 or maintain\$3) same static\$10 adj3 thread same linked list	0
	<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L18	(stor\$6 or maintain\$3) same static\$10 adj3 thread same linked list	0
	<i>DB=USPT; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L17	thread same (stor\$6 or maintain\$3) same static\$10 adj3 thread	9
	<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L16	thread same (stor\$6 or maintain\$3) with static\$4allocated thread	0
<input type="checkbox"/>	L15	thread block with (stor\$6 or maintained) with static\$4allocated thread	0
<input type="checkbox"/>	L14	L13 and l12	0
<input type="checkbox"/>	L13	dynamic\$ allocat\$3 near5 thread	63
<input type="checkbox"/>	L12	static\$6 allocat\$3 near5 thread	15
<input type="checkbox"/>	L11	static\$6-allocat\$3 near5 thread	0
<input type="checkbox"/>	L10	static\$6-allocated near5 thread	0
<input type="checkbox"/>	L9	static\$6-allocated adj thread	0
<input type="checkbox"/>	L8	static\$6 allocated adj thread	0
<input type="checkbox"/>	L7	L6 and simulat\$6	2
<input type="checkbox"/>	L6	l4 and l5	12
<input type="checkbox"/>	L5	dynamic\$\$ adj3 thread	229
<input type="checkbox"/>	L4	static\$ adj3 thread	116
<input type="checkbox"/>	L3	simulat\$3 same static\$ adj3 thread	0
<input type="checkbox"/>	L2	simulat\$3 same static\$ adj3 thread same dynamic\$\$ adj3 thread	0
<input type="checkbox"/>	L1	simulat\$3 with static\$ adj3 thread same dynamic\$\$ adj3 thread	0

END OF SEARCH HISTORY

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Freeform Search

Database:	US Pre-Grant Publication Full-Text Database US Patents Full-Text Database US OCR Full-Text Database EPO Abstracts Database JPO Abstracts Database Derwent World Patents Index IBM Technical Disclosure Bulletins
Term:	5835763[uref] <div style="float: right; text-align: center;"> <input type="button" value="Up"/> <input type="button" value="Down"/> </div>
Display: <input type="text" value="10"/> Documents in Display Format: <input type="text" value="TI"/> Starting with Number <input type="text" value="1"/>	
Generate: <input type="radio"/> Hit List <input checked="" type="radio"/> Hit Count <input type="radio"/> Side by Side <input type="radio"/> Image	

Search History

DATE: Monday, September 27, 2004 [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

Hit Count Set Name

result set

DB=USPT; PLUR=YES; OP=ADJ

<u>L27</u> 5835763[uref]	6	<u>L27</u>
<u>L26</u> 5835763.pn.	1	<u>L26</u>
<u>L25</u> L24 and l21	8	<u>L25</u>
<u>L24</u> thread same simulat\$4	897	<u>L24</u>
<u>L23</u> L22 and static\$6 adj3 thread	1	<u>L23</u>
<u>L22</u> l21 and thread schedul\$4	45	<u>L22</u>
<u>L21</u> 718/102.ccls.	668	<u>L21</u>
<u>L20</u> static\$10 adj3 thread same linked list	0	<u>L20</u>

DB=USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ

<u>L19</u> (stor\$6 or maintain\$3) same static\$10 adj3 thread same linked list	0	<u>L19</u>
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DB=PGPB,USPT; PLUR=YES; OP=ADJ

<u>L18</u> (stor\$6 or maintain\$3) same static\$10 adj3 thread same linked list	0	<u>L18</u>
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DB=USPT; PLUR=YES; OP=ADJ

<u>L17</u> thread same (stor\$6 or maintain\$3) same static\$10 adj3 thread	9	<u>L17</u>
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DB=PGPB,USPT; PLUR=YES; OP=ADJ

<u>L16</u> thread same (stor\$6 or maintain\$3) with static\$4allocated thread	0	<u>L16</u>
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<u>L15</u>	thread block with (stor\$6 or maintained) with static\$4allocated thread	0	<u>L15</u>
<u>L14</u>	L13 and l12	0	<u>L14</u>
<u>L13</u>	dynamic\$ allocat\$3 near5 thread	63	<u>L13</u>
<u>L12</u>	static\$6 allocat\$3 near5 thread	15	<u>L12</u>
<u>L11</u>	static\$6-allocat\$3 near5 thread	0	<u>L11</u>
<u>L10</u>	static\$6-allocated near5 thread	0	<u>L10</u>
<u>L9</u>	static\$6-allocated adj thread	0	<u>L9</u>
<u>L8</u>	static\$6 allocated adj thread	0	<u>L8</u>
<u>L7</u>	L6 and simulat\$6	2	<u>L7</u>
<u>L6</u>	l4 and l5	12	<u>L6</u>
<u>L5</u>	dynamic\$\$ adj3 thread	229	<u>L5</u>
<u>L4</u>	static\$ adj3 thread	116	<u>L4</u>
<u>L3</u>	simulat\$3 same static\$ adj3 thread	0	<u>L3</u>
<u>L2</u>	simulat\$3 same static\$ adj3 thread same dynamic\$\$ adj3 thread	0	<u>L2</u>
<u>L1</u>	simulat\$3 with static\$ adj3 thread same dynamic\$\$ adj3 thread	0	<u>L1</u>

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<input type="checkbox"/>	L8	static\$6 allocated adj thread	0
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<input type="checkbox"/>	L3	simulat\$3 same static\$ adj3 thread	0
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<input type="checkbox"/>	L1	simulat\$3 with static\$ adj3 thread same dynamic\$\$ adj3 thread	0

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